

Vision of the Institution

To ignite the minds of the students through academic excellence so as to bring about social transformation and prosperity.

Mission of the Institution

- To expand the frontiers of knowledge through Quality Education.
- To provide valued added Research and Development.
- To embody a spirit of excellence in Teaching, Creativity, Scholarship and Outreach.
- To provide a platform for synergy of Academy, Industry and Community.
- To inculcate high standards of Ethical and Professional Behavior.

Vision of ECE Department

In pursuit of world class excellence in the field of Electronics & Communication Engineering by imparting quality education and promoting Research.

Mission of ECE Department

- To empower students with knowledge and competencies in the field of Electronics & Communication Engineering conforming to International standards.
- To produce creative solutions essential to local and global needs in the field of Electronics & Communication Engineering.
- To mould the students professionally with a consciousness of moral values and professional ethical code.

Program Educational Objectives (PEOs) of ECE Department

PEO1: To provide world class Education in the principles of engineering that incorporate open ended design experience and the use of software and hardware tools related to Electronics and Communication Engineering and hence improve the employability skills of the student.

PEO2: To make the students able to function with multi-disciplinary teams that will enhance the leadership qualities and to formulate and solve engineering problems as a team which helps the student to adopt better professional conduct.

PEO3: To provide learning environment that provides open interaction for the students with faculty and staff that makes them innovative and dynamic and encourages research and motivate them to solve the problems of the society.

Program Outcomes (POs) of ECE Department

Engineering Graduates will be able to:

1. **Engineering knowledge:** Apply the knowledge of mathematics, science, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
2. **Problem analysis:** Identify, formulate, review research literature, and analyze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural sciences, and engineering sciences.
3. **Design/development of solutions:** Design solutions for complex engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal, and environmental considerations.
4. **Conduct investigations of complex problems:** Use research-based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
5. **Modern tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations.
6. **The engineer and society:** Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
7. **Environment and sustainability:** Understand the impact of the professional engineering solutions in societal and environmental contexts, and demonstrate the knowledge of, and need for sustainable development.
8. **Ethics:** Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
9. **Individual and team work:** Function effectively as an individual, and as a member or leader in diverse teams, and in multidisciplinary settings.
10. **Communication:** Communicate effectively on complex engineering activities with the engineering community and with society at large, such as, being able to comprehend and write effective reports and design documentation, make effective presentations, and give and receive clear instructions.

11. **Project management and finance:** Demonstrate knowledge and understanding of the engineering and management principles and apply these to one's own work, as a member and leader in a team, to manage projects and in multidisciplinary environments.
12. **Life-long learning:** Recognize the need for, and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

Program Specific Outcomes (PSOs) of ECE Department

1. Will be equipped with knowledge of innovative, dynamic complete design flow specialized in implementation of projects pertaining to communication system, signal processing, digital and analog IC design, embedded systems and will integrate all areas to illustrate the goal of digital India.
2. Will have the ability to analyze, design electronics and communication applications using software tools like, pSpice, XYLINX, MATLAB, Mentor Graphics and other related software's.
3. Can demonstrate the principles of semiconductor devices, digital system, Microprocessor and microcontrollers, signal processing, antenna design in fields of consumer electronics, medical, defence and spacecraft electronics industry.
4. Will have strong ethical moral values and sound fundamental foundation of technical knowledge in all core subjects which help them to explore scientific theories, ideas, methods and technologies that help in solving current and future universal societal problems through Assistive Technology Laboratory as a flat form.

III Year-I Semester

L	T	P	C
4	0	0	3

COMPUTER ARCHITECTURE AND ORGANIZATION

OBJECTIVES:

- Understand the architecture of a modern computer with its various processing units. Also the Performance measurement of the computer system.
- In addition to this the memory management system of computer.

UNIT-I:

Basic Structure Of Computers: Functional unit, Basic Operational concepts, Bus structures, System Software, Performance, The history of computer development.

UNIT-II:

Machine Instruction and Programs:

Instruction and Instruction Sequencing: Register Transfer Notation, Assembly Language Notation, Basic Instruction Types, Addressing Modes, Basic Input/output Operations, The role of Stacks and Queues in computer programming equation. Component of Instructions: Logic Instructions, shift and Rotate Instructions

UNIT-III:

Type of Instructions: Arithmetic and

Logic Instructions, Branch Instructions, Addressing Modes, Input/output Operations

UNIT-IV:

INPUT/OUTPUT ORGANIZATION: Accessing I/O Devices, Interrupts: Interrupt Hardware, Enabling and Disabling Interrupts, Handling Multiple Devices, Direct Memory Access,

Buses: Synchronous Bus, Asynchronous Bus, Interface Circuits, Standard I/O Interface: Peripheral Component Interconnect (PCI) Bus, Universal Serial Bus (USB)

UNIT-V:

The MEMORY SYSTEMS: Basic memory circuits, Memory System Consideration, Read-

Only Memory: ROM, PROM, EPROM, EEPROM, Flash Memory,

Cache Memories: Mapping Functions, INTERLEAVING

Secondary Storage: Magnetic Hard Disks, Optical Disks,

UNIT-VI:

Processing Unit: Fundamental Concepts: Register Transfers, Performing An Arithmetic Or Logic Operation, Fetching A Word From Memory,

Execution of Complete Instruction, Hardwired Control,

Microprogrammed Control: Microinstructions, Microprogram Sequencing, Wide Branch Addressing Microinstructions with next –Address Field

TEXTBOOKS:

1. Computer Organization, Carl Hamacher, Zvonks Vranesic, Safea Zaky, 5th Edition, McGraw Hill.
2. Computer Architecture and Organization, John P. Hayes, 3rd Edition, McGraw Hill.

REFERENCEBOOKS:

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/Pearson
3. Fundamentals of Computer Organization and Design, -Sivaraama Dandamudi Springer Int. Edition.
4. “Computer Organization and Design: The Hardware/Software Interface” by David A. Patterson and John L. Hennessy.
5. J .P. Hayes, "Computer Architecture and Organization", McGraw-Hill, 1998.

OUTCOMES:

- Students can understand the architecture of modern computer.
- They can analyze the performance of a computer using performance equation
- Understanding of different instruction types.
- 4. Students can calculate the effective address of an operand by addressing modes
- 5. They can understand how computer stores positive and negative numbers.
- 6. Understanding of how a computer performs arithmetic operation of positive and negative numbers.

III Year-I Semester

L	T	P	C
4	0	0	3

LINEAR IC APPLICATIONS

OBJECTIVES

- To understand the basic operation & performance parameters of differential amplifiers.
- To understand & learn the measuring techniques of performance parameters of OP-AMP
- To learn the linear and non-linear applications of operational amplifiers.
- To understand the analysis & design of different types of active filters using opamps
- To learn the internal structure, operation and applications of different analog ICs
- To Acquire skills required for designing and testing integrated circuits

UNIT I

INTEGRATED CIRCUITS: Differential Amplifier-DC and AC Analysis of Dual Input Balanced output Configuration, Properties of other differential amplifier configuration (Dual Input Unbalanced Output, Single Ended Input–Balanced/Unbalanced Output), DC Coupling and Cascade Differential Amplifier Stages, Level translator.

UNIT II

Characteristics of OP-Amps, Integrated circuits-Types, Classification, Package Types and Temperature ranges, Power supplies, Op-amp Block Diagram, ideal and practical Op-amp Specifications, DC and AC characteristics, 741 op-amp & its features, Op-Amp parameters & Measurement, Input & Output Offset voltages & currents, slewrate, CMRR, PSRR, drift, Frequency Compensation techniques.

UNIT III

LINEAR and NON-LINEAR APPLICATIONS OF OP-AMPS: Inverting and Non-inverting amplifier, Integrator and differentiator, Difference amplifier, Instrumentation amplifier, AC amplifier, V to I, I to V converters, Buffers. Non-Linear function generation, Comparators, Multivibrators, Triangular and Square wave generators, Log and Antilog Amplifiers, Precision rectifiers.

UNIT IV

ACTIVE FILTERS, ANALOG MULTIPLIERS AND MODULATORS: Design & Analysis of Butterworth active filters – 1st order, 2nd order LPF, HPF filters. Band pass, Band reject and all pass filters. Four Quadrant Multiplier, IC 1496, Sample & Hold circuits.

UNIT V

TIMERS & PHASE LOCKED LOOPS: Introduction to 555 timer, functional diagram, Monostable and Astable operations and applications, Schmitt Trigger; PLL - introduction, block schematic, principles and description of individual blocks, 565 PLL, Applications of PLL – frequency multiplication, frequency translation, AM, FM & FSK demodulators. Applications of VCO (566).

UNIT VI

DIGITAL TO ANALOG AND ANALOG TO DIGITAL CONVERTERS: Introduction, basic DAC techniques, weighted resistor DAC, R-2R ladder DAC, inverted R-2R DAC, and IC 1408 DAC, Different types of ADCs – parallel Comparator type ADC, counter type ADC, successive approximation ADC and dual slope ADC. DAC and ADC Specifications, Specifications AD574 (12 bit ADC).

TEXTBOOKS:

1. Linear Integrated Circuits–D.Roy Choudhury, New Age International (p) Ltd, 2nd Edition, 2003.
2. Op-Amps & Linear ICs–Ramakanth A. Gayakwad, PHI, 1987.
3. Operational Amplifiers–C.G. Clayton, Butterworth & Company Publ. Ltd./Elsevier, 1971

REFERENCES:

1. Operational Amplifiers & Linear Integrated Circuits–Sanjay Sharma; SK Kataria & Sons; 2nd Edition, 2010
2. Design with Operational Amplifiers & Analog Integrated Circuits–Sergio Franco, McGraw Hill, 1988.
3. OPAMPS and Linear Integrated Circuits concepts and Applications, James M Fiore, Cengage Learning India Ltd.
4. Operational Amplifiers & Linear Integrated Circuits–R.F. Coughlin & Fredrick Driscoll, PHI, 6th Edition.
5. Operational Amplifiers & Linear ICs–David A Bell, Oxford Uni. Press, 3rd Edition

OUTCOMES

- Design circuits using operational amplifiers for various applications.
- Analyze and design amplifiers and active filters using Op-amp.
- Diagnose and trouble-shoot linear electronic circuits.
- Understand the gain-bandwidth concept and frequency response of the amplifier configurations.
- Understand thoroughly the operational amplifiers with linear integrated circuits.

DIGITAL IC APPLICATIONS

OBJECTIVES

The main objectives of this course are:

- Introduction of digital logic families and interfacing concepts for digital design is considered.
- VHDL fundamentals are discussed to model the digital system design blocks.
- VHDL compilers, simulators and synthesis tools are described, which are used to verify digital systems in a technology-independent fashion.
- Design and implementation of combinational and sequential digital logic circuits is explained.

Syllabus:

UNIT-I

Digital Logic Families and Interfacing: Introduction to logic families, CMOS logic, CMOS steady state and dynamic electrical behavior, CMOS logic families. Bipolar logic, transistor-transistor logic, TTL families, CMOS/TTL interfacing, low voltage CMOS logic and interfacing, Emitter coupled logic.

(Textbook-1)

UNIT-II

Introduction to VHDL: Design flow, program structure, levels of abstraction, Elements of VHDL: Data types, data objects, operators and identifiers. Packages, Libraries and Bindings, Subprograms. VHDL Programming using structural and data flow modeling.

(Textbook-2)

UNIT-III

Behavioral Modeling: Process statement, variable assignment statement, signal assignment statement, wait statement, if statement, case statement, null statement, loop statement, exit statement, next statement, assertion statement, more on signal assignment statement, Inertial Delay Model, Transport Delay Model, Creating Signal Waveforms, Signal Drivers, Other Sequential Statements, Multiple Processes. Logic Synthesis, Inside a logic Synthesizer.

(Textbook-2)

UNIT-IV

Combinational Logic Design: Binary Adder-Subtractor, Ripple Adder, Look Ahead Carry Generator, ALU, Decoders, encoders, multiplexers and demultiplexers, parity circuits, comparators, Barrel Shifter, Simple Floating-Point Encoder, Dual Priority Encoder, Design considerations of the above combinational logic circuits with relevant Digital ICs, modeling of above ICs using VHDL.

(Textbook-1)

UNIT-V

Sequential Logic Design: SSI Latches and flip flops, Ring Counter, Johnson Counter, Design of Modulus N Synchronous Counters, Shift Registers, Universal Shift Registers, Design considerations of the above sequential logic circuits with relevant Digital ICs, modeling of above ICs using VHDL.

(Textbook-1)

UNIT-VI:

Synchronous and Asynchronous Sequential Circuits: Basic design steps: State diagram, state table, state assignment, choice of flip flops and derivation of next state and output expressions, timing diagram. State assignment problem: One hot encoding. Mealy and Moore type FSM for serial adder, VHDL code for the serial adder. Analysis of Asynchronous circuits, State Reduction, State Assignment. A complete design example: The vending machine controller.

(Referencetextbook-1)

TextBooks:

1. Digital Design Principles & Practices – John F. Wakerly, PHI/Pearson Education Asia, 3rd Ed., 2005.
2. VHDL Primer – J. Bhasker, Pearson Education/PHI, 3rd Edition.

References:

1. Fundamentals of Digital Logic with VHDL Design – Stephen Brown, Zvonko Vranesic, McGraw Hill, 3rd Edition.

Outcomes:**At the end of this course the student can able to:**

- Understand the structure of commercially available digital integrated circuit families.
- Learn the IEEE Standard 1076 Hardware Description Language (VHDL).
- Model complex digital systems at several levels of abstractions, behavioral, structural, simulation, synthesis and rapid system prototyping.
- Analyze and design basic digital circuits with combinatorial and sequential logic circuits using VHDL.

III Year-I Semester

L	T	P	C
4	0	0	3

DIGITAL COMMUNICATIONS

Course Objectives:

1. Understand different pulse digital modulation techniques and their comparison
2. Familiarize various digital modulation techniques and calculation of their error probabilities
3. Understand the concept of entropy and different source coding techniques
4. Familiarize with block codes, cyclic codes and convolutional codes

UNIT I

PULSEDIGITALMODULATION: Elements of digital communication systems, advantages of digital communication systems, Elements of PCM: Sampling, Quantization & Coding, Quantization error, Companding in PCM systems. Differential PCM systems (DPCM). Delta modulation, its drawbacks, adaptive delta modulation, comparison of PCM and DM systems, noise in PCM and DM systems.

UNIT II

DIGITALMODULATIONTECHNIQUES: Introduction, ASK, FSK, PSK, DPSK, DEPSK, QPSK, M-ary PSK, ASK, FSK, similarity of BFSK and BPSK.

UNIT III

DATA TRANSMISSION : Base band signal receiver, probability of error, the optimum filter, matched filter, probability of error using matched filter, coherent reception, non-coherent detection of FSK, calculation of error probability of ASK, BPSK, BFSK, QPSK.

UNIT IV

INFORMATION THEORY: Discrete messages, concept of amount of information and its properties. Average information, Entropy and its properties. Information rate, Mutual information and its properties.

UNIT V

SOURCE CODING: Introductions, Advantages, Shannon's theorem, Shannon-Fano coding, Huffman coding, efficiency calculations, channel capacity of discrete and analog Channels, capacity of a Gaussian channel, bandwidth-S/N tradeoff.

UNIT VI

LINEAR BLOCK CODES: Introduction, Matrix description of Linear Block codes, Error detection and error correction capabilities of Linear block codes, Hamming codes, Binary cyclic codes, Algebraic structure, encoding, syndrome calculation, BCH Codes.

CONVOLUTION CODES: Introduction, encoding of convolution codes, time domain approach, transform domain approach. Graphical approach: state, tree and trellis diagram decoding using Viterbi algorithm.

TEXTBOOKS:

1. Digital communications-Simon Haykin, John Wiley, 2005
2. Principles of Communication Systems-H. Taub and D. Schilling, TMH, 2003

REFERENCES:

1. Digital and Analog Communication Systems-Sam Shanmugam, John Wiley, 2005.
2. Digital Communications-John Proakis, TMH, 1983. Communication Systems Analog & Digital-Singh & Sapre, TMH, 2004.
3. Modern Analog and Digital Communication-B.P. Lathi, Oxford reprint, 3rd edition, 2004.

Course Outcomes:

After undergoing the course students will be able to:

1. Determine the performance of different waveform coding techniques for the generation and digital representation of the signals.
2. Determine the probability of error for various digital modulation schemes
3. Analyze different source coding techniques
4. Compute and analyze different error control coding schemes for the reliable transmission of digital information over the channel.

III Year-I Semester

L	T	P	C
4	0	0	3

ANTENNA AND WAVE PROPAGATION

OBJECTIVES

The student will be able to

- understand the applications of the electromagnetic waves in free space.
- introduce the working principles of various types of antennas
- discuss the major applications of antennas with an emphasis on how antennas are employed to meet electronics system requirements.
- understand the concepts of radio wave propagation in the atmosphere.

UNIT I

ANTENNA FUNDAMENTALS: Introduction, Radiation Mechanism – single wire, 2 wire, dipoles, Current Distribution on a thin wire antenna. Antenna Parameters - Radiation Patterns, Patterns in Principal Planes, Main Lobe and Side Lobes, Beamwidths, Polarization, Beam Area, Radiation Intensity, Beam Efficiency, Directivity, Gain and Resolution, Antenna Apertures, Aperture Efficiency, Effective Height, illustrated Problems.

UNIT II

THIN LINEAR WIRE ANTENNAS: Retarded Potentials, Radiation from Small Electric Dipole, Quarter wave Monopole and Half wave Dipole – Current Distributions, Evaluation of Field Components, Power Radiated, Radiation Resistance, Beamwidths, Directivity, Effective Area and Effective Height. Natural current distributions, fields and patterns of Thin Linear Center-fed Antennas of different lengths, Radiation Resistance at a point which is not current maximum. Antenna Theorems – Applicability and Proofs for equivalence of directional characteristics, Loop Antennas: Small Loops - Field Components, Comparison of far fields of small loop and short dipole, Concept of short magnetic dipole, D and R_r relations for small loops.

UNIT III

ANTENNA ARRAYS : 2 element arrays – different cases, Principle of Pattern Multiplication, N element Uniform Linear Arrays – Broadside, End-fire Arrays, EFA with Increased Directivity, Derivation of their characteristics and comparison; Concept of Scanning Arrays. Directivity Relations (no derivations). Related Problems. Binomial Arrays, Effects of Uniform and Non-uniform Amplitude Distributions, Design Relations. Arrays with Parasitic Elements, Yagi-Uda Arrays, Folded Dipoles and their characteristics.

UNIT IV

NON-RESONANT RADIATORS : Introduction, Traveling wave radiators – basic concepts, Long wire antennas – field strength calculations and patterns, Microstrip Antennas-Introduction, Features, Advantages and Limitations, Rectangular Patch Antennas – Geometry and Parameters, Impact of different parameters on characteristics. Broadband Antennas: Helical Antennas – Significance, Geometry, basic properties; Design considerations for monofilar helical antennas in Axial Mode and Normal Modes (Qualitative Treatment).

UNIT V

VHF, UHF AND MICROWAVE ANTENNAS: Reflector Antennas: Flat Sheet and Corner Reflectors. Paraboloidal Reflectors – Geometry, characteristics, types of feeds, F/D Ratio, Spill Over, Back Lobes, Aperture Blocking, Offset Feeds, Cassegrain Feeds.

Horn Antennas – Types, Optimum Horns, Design Characteristics of Pyramidal Horns; Lens Antennas – Geometry, Features, Dielectric Lenses and Zoning, Applications, Antenna Measurements – Patterns Required, Set Up, Distance Criterion, Directivity and Gain Measurements (Comparison, Absolute and 3-Antenna Methods).

UNIT VI

WAVE PROPAGATION : Concepts of Propagation – frequency ranges and types of propagations. Ground Wave Propagation – Characteristics, Parameters, Wave Tilt, Flat and Spherical Earth Considerations. Sky Wave Propagation – Formation of Ionospheric Layers and their Characteristics, Mechanism of Reflection and Refraction, Critical Frequency, MUF and Skip Distance – Calculations for flat and spherical earth cases, Optimum Frequency, LUF, Virtual Height, Ionospheric Abnormalities, Ionospheric Absorption.

Fundamental Equation for Free-Space Propagation, Basic Transmission Loss Calculations. Space Wave Propagation – Mechanism, LOS and Radio Horizon. Tropospheric Wave Propagation – Radius of Curvature of path, Effective Earth's Radius, Effect of Earth's Curvature, Field Strength Calculations, M-curves and Duct Propagation, Tropospheric Scattering.

TEXTBOOKS

1. Antennas for All Applications – John D. Kraus and Ronald J. Marhefka, 3rd Edition, TMH, 2003.
2. Electromagnetic Waves and Radiating Systems – E. C. Jordan and K. G. Balmain, PHI, 2nd Edition, 2000.

REFERENCES

1. Antenna Theory – C. A. Balanis, John Wiley and Sons, 2nd Edition, 2001.
2. Antennas and Wave Propagation – K. D. Prasad, Satya Prakashan, Tech India Publications, New Delhi, 2001.
3. Transmission and Propagation –
E. V. D. Glazier and H. R. L. Lamont, The Services Text Book of Radio, vol. 5, Standard Publishers Distributors, Delhi.
4. Electronic and Radio Engineering – F. E. Terman, McGraw-Hill, 4th Edition, 1955.
5. Antennas – John D. Kraus, McGraw-Hill, 2nd Edition, 1988.

OUTCOMES

After going through this course the student will be able to

- Identify basic antenna parameters.
- Design and analyze wire antennas, loop antennas, reflector antennas, lens antennas, horn antennas and microstrip antennas
- Quantify the fields radiated by various types of antennas
- Design and analyze antenna arrays
- Analyze antenna measurements to assess antenna's performance
- Identify the characteristics of radio wave propagation

III Year-I Semester

L	T	P	C
0	0	3	2

PULSE&DIGITALCIRCUITSLAB

- 1. Linear waveshaping.**
- 2. NonLinear waveshaping–Clippers.**
- 3. NonLinear waveshaping–Clampers.**
- 4. Transistor as a switch.**
- 5. Study of Logic Gates & Some applications.**
- 6. Study of Flip-Flops & some applications.**
- 7. Sampling Gates.**
- 8. Astable Multivibrator.**
- 9. Monostable Multivibrator.**
- 10. Bistable Multivibrator.**
- 11. Schmitt Trigger.**
- 12. UJT Relaxation Oscillator.**
- 13. Bootstrap sweep circuit.**

Equipment required for Laboratory:1.

RPS -0 – 30 V

2. CRO-0– 20 MHz.

3. Function Generators-0–1MHz

4. Components

5. MultiMeters

III Year-I Semester

L	T	P	C
0	0	3	2

LIC APPLICATIONS LAB

Minimum Twelve Experiments to be conducted:

1. Study of OPAMPs – IC741, IC555, IC565, IC566, IC1496 – functioning, parameters and Specifications.
2. OPAMP Applications – Adder, Subtractor, Comparator Circuits.
3. Integrator and Differentiator Circuits using IC741.
4. Active Filter Applications – LPF, HPF (first order)
5. Active Filter Applications – BPF, Band Reject (Wideband) and Notch Filters.
6. IC741 Oscillator Circuits – Phase Shift and Wien Bridge Oscillators.
7. Function Generator using OPAMPs.
8. IC555 Timer – Monostable Operation Circuit.
9. IC555 Timer – Astable Operation Circuit.
10. Schmitt Trigger Circuits – using IC741 and IC555.
11. IC565 – PLL Applications.
12. IC566 – VCO Applications.
13. Voltage Regulator using IC723.
14. Three Terminal Voltage Regulators – 7805, 7809, 7912.

Equipment required for Laboratories:

1. RPS
2. CRO
3. Function Generator
4. Multi Meters
5. IC Trainer Kits (Optional)
6. Bread Boards
7. Components: -IC741, IC555, IC565, IC1496, IC723, 7805, 7809, 7912 and other essential components.
8. Analog IC Tester

III Year-I Semester

L	T	P	C	
	0	0	3	2

DICALABORATORY

Note: The students are required to design and draw the internal logical structure of the following Digital Integrated Circuits and to develop VHDL/Verilog HDL Source code, perform simulation using relevant simulator and analyze the obtained simulation results using necessary synthesizer.

All the experiments are required to verify and implement the logical operations on the latest FPGA Hardware in the Laboratory.

List of Experiments: (Minimum of Ten Experiments has to be performed)

1. Realization of Logic Gates
2. Design of Full Adder using 3 modeling systems
3. 3 to 8 Decoder-74138
4. 8 to 3 Encoder (with and without parity)
5. 8x 1 Multiplexer-74151 and 2x 4 De-multiplexer-74155
6. 4-Bit Comparator-7485
7. D Flip-Flop-7474
8. Decade Counter-7490
9. Shift Registers-7495
10. 8-bit serial in-parallel out and parallel in-serial out
11. Fast In & Fast Out (FIFO)
12. MAC (Multiplier & Accumulator)
13. ALU Design.

Equipment/Software required:

1. Xilinx Vivado software/Equivalent Industry Standard Software
2. Xilinx Hardware/Equivalent hardware
3. Personal computer system with necessary software to run the programs and Implement.